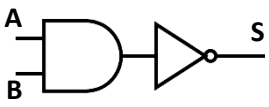
Unit: Electronics. Logic gates exercises.

Name: _____ Date: _____

Fill in the output column “S” for each of the logic gates circuits.

1)



A	B	S
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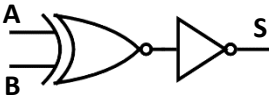
0	1	
1	0	
1	1	

2)



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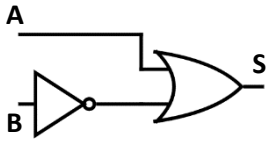
3)



A	B	S
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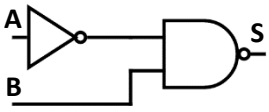
1	1	

4)



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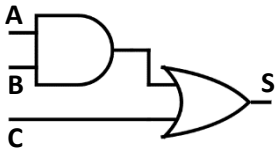
1	1	

5)



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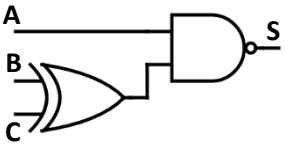
1	1	1	

6)



A	B	C	S
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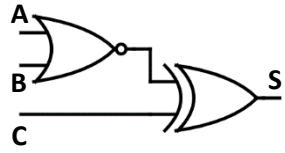
7)



A	B	C	S
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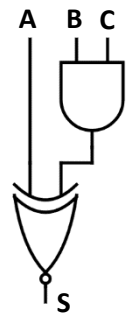
8)



A	B	C	S
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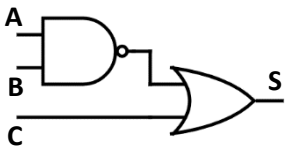
9)



A	B	C	S
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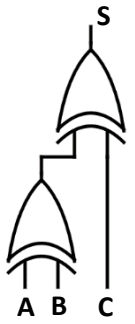
10)



A	B	C	S
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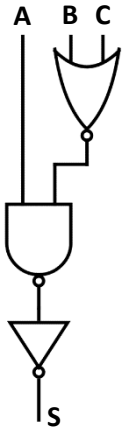
11)



A	B	C	S
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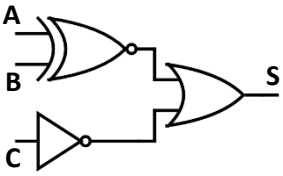
12)



A	B	C	S
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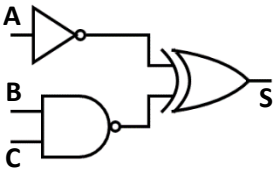
1	1	1	

13)



A	B	C	S
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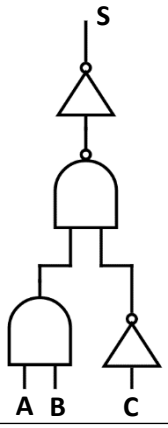
1	1	0	
1	1	1	

14)



A	B	C	S
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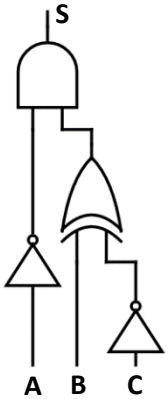
15)



A	B	C	S
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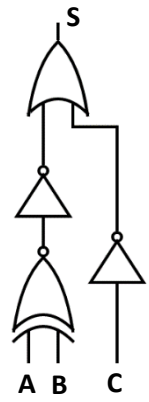
16)



A	B	C	S
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0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	